

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)

K06-159164M/AT

Application Number

~~Not Yet Assigned~~

10 668254

Applicant(s)

Yasuji NORITO

Filing Date

Concurrently Herewith

Group Art Unit

~~Not Yet Assigned~~

28 63

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
TL		5-28521	07/22/1993	Japan				✓

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

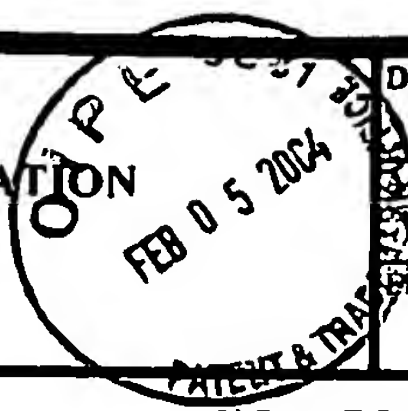
TyM

DATE CONSIDERED

12-16-04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)



Docket Number (Optional) K06-159164M/AT		Application Number 10/668,254
Applicant(s) Yasuji Norito		
Filing Date September 24, 2003		Group Art Unit 2816

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
tl		6,198,239 B1	03/06/2001	Blackburn			
tl		6,304,088 B1	10/16/2001	Yee			
tl		3,642,329	02/15/1972	Zechall et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
tl		10-197572	07/31/1998	Japan			(ABS only)	
		FR 2 817 970	06/14/2002	France				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		European Search Report dated January 14, 2004.
		"Voltage Comparator Circuit", IBM Technical Disclosure Bulletin, Vol. 17, No. 4, September 1974, pp. 1151-1152, XP-002265879.

EXAMINER <i>tyu</i>	DATE CONSIDERED <i>12-10-04</i>
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